

1/7

FIG.1

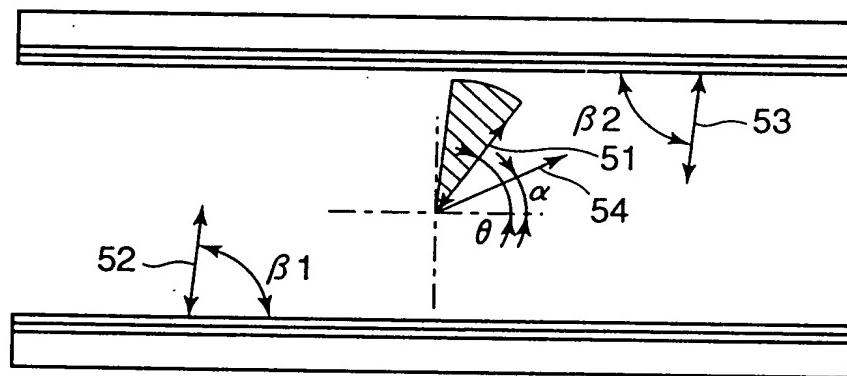
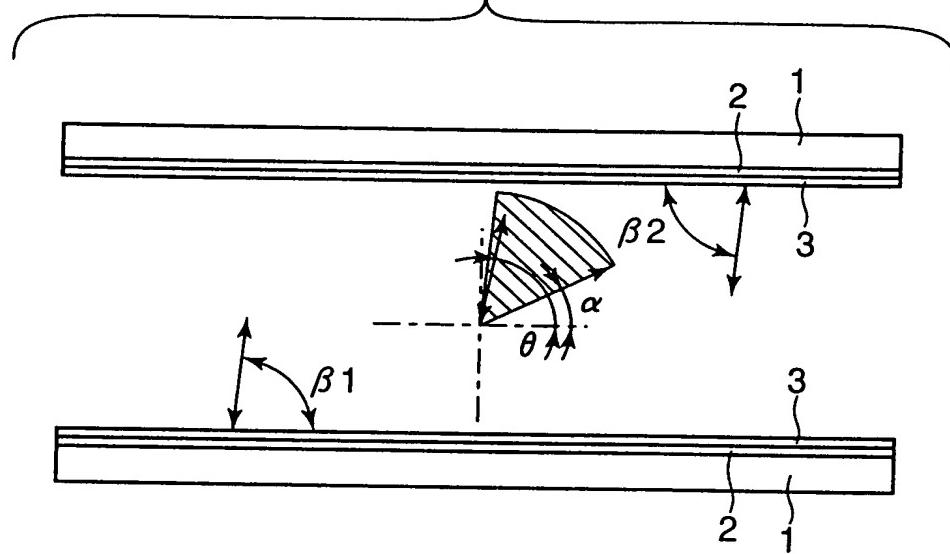




FIG.2

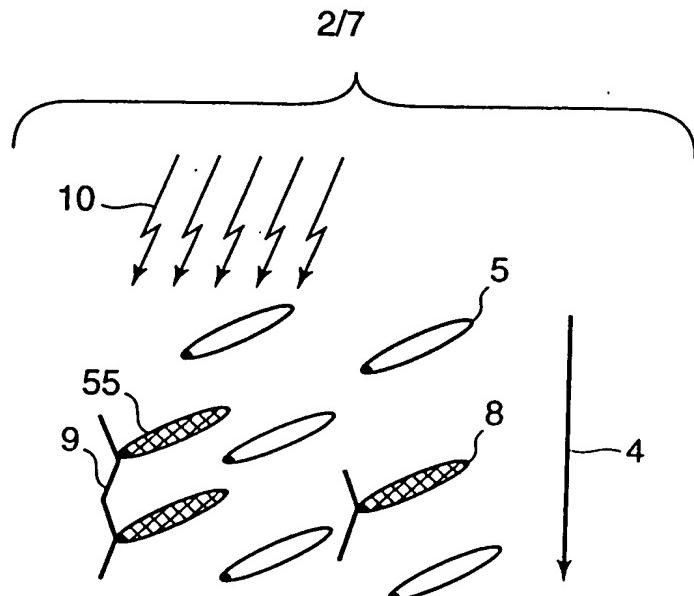
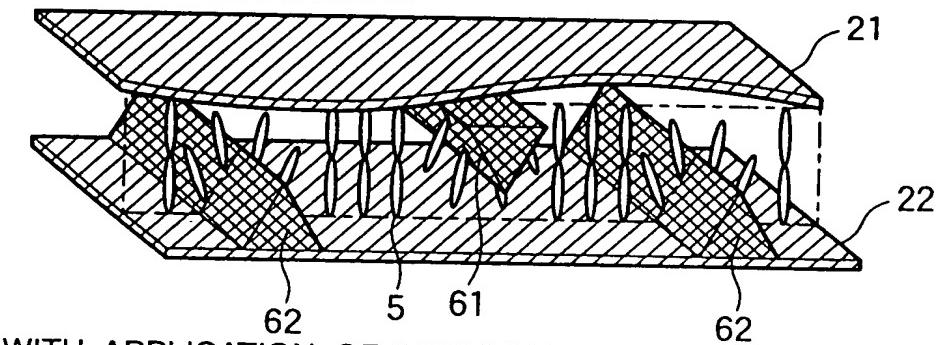


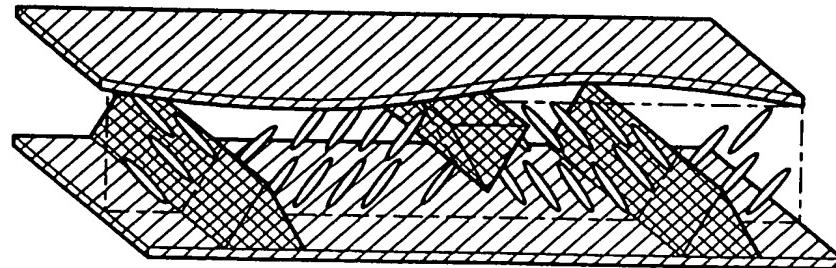
FIG.3

PRIOR ART

WITHOUT APPLICATION OF VOLTAGE
BETWEEN ELECTRODES



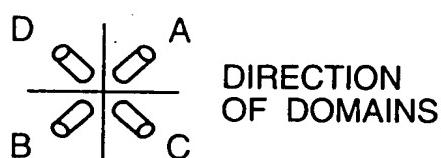
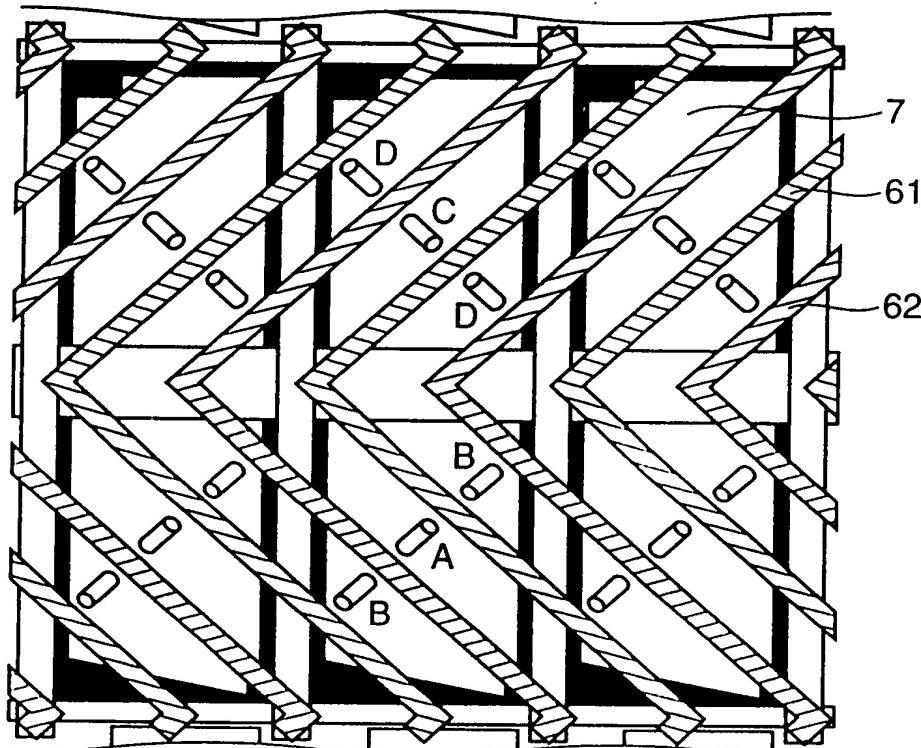
WITH APPLICATION OF VOLTAGE



3/7

PRIOR ART

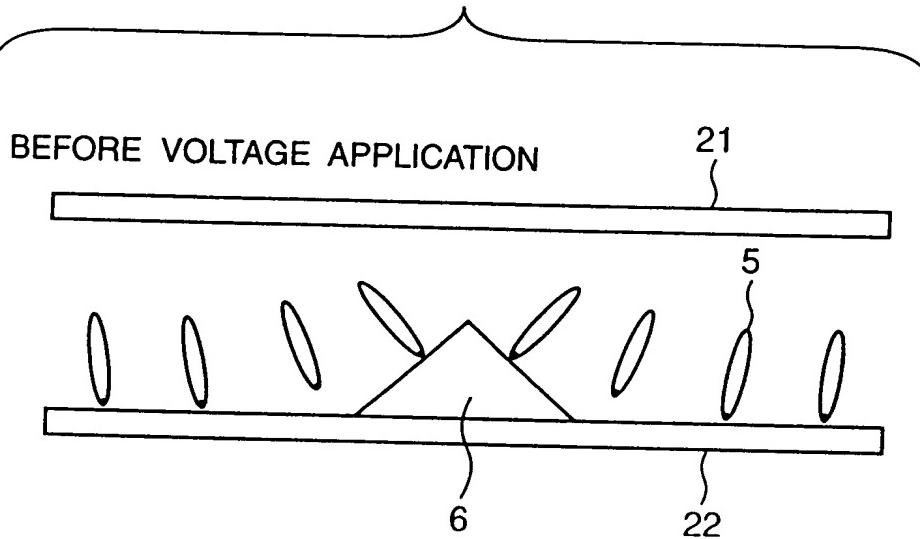
FIG.4



4/7

PRIOR ART

FIG.5



AFTER PASSAGE OF PREDETERMINED TIME FROM VOLTAGE APPLICATION

